

Model	Channels	Resolution	Functionality
LX3V-2ADI2DAI-BD	4	12 bits	2 * Analog Input/ Analog Output (4~20mA)
LX3V-2ADV2DAV-BD	4	12 bits	2 * Analog Input/ Output (-10V~10V)

Specification

Item	LX3V-2ADI2DAI-BD	LX3V-2ADV2DAV-BD
Power supply	24VDC±10%, 50mA; 5VDC±10%, 70mA (Powered by PLC host)	
Analog input range	DC 4~20mA (input resistance 250KΩ). Absolute maximum input: -2mA, +30mA	DC-10V~10V (input resistance 160KΩ). Note: If the input voltage exceeds ±15V, the unit will be damaged.
Rated range	4~20mA: 0~2000	-10V~10V: -2000~2000
The maximum display range	-500~2048	-2048~2048
Resolution	8uA[4mA ~20mA / 2000] ±0.5% of full scale (4~20mA:±0.08mA)	5mV[10V default scope 1/2000] ±0.5% of full scale
AD conversion time	One PLC scanning cycle	
Input characteristics		
Insulation	No insulation in each PLC channel	
Occupied points	None	
Analog output (DAI)	4~20mA: 0~2000	Analog output (DAV)
Rated range	DC 4~20mA (the external load resistance is no less than 500Ω)	-10V~10V: -2000~2000
Analog output	DC-10V~10V (the external load resistance is no less than 2KΩ)	
Digital output	12 bit binary	
Resolution	8uA[4~20mA / 2000]	5mV[10V default scope 1/2000]
Precision	±0.5% of full scale	
AD conversion time	One PLC scanning cycle	
Input characteristics		
Insulation	No insulation in each PLC channel	
Occupied points	None	

